


Brief CV

Name	Siti Hawa Ruslan	中文名		
Gender	Female	Title (Pro./Dr.)	Associate Professor	
Position (President...)	Associate Professor	Country	Malaysia	
University/ Department	Universiti Tun Hussein Onn Malaysia Electronic Engineering Department, Faculty of Electrical and Electronic Engineering			
Personal Website	https://community.uthm.edu.my/sitihawa			
Research Area	VLSI design, low power IC design, analog and digital system design			

Brief introduction of your research experience:

Has involved in several research fields. Basically it is more on VLSI design be it analog or digital design. Also has several research grants either as the leader or a member for the research group. For example a leader for the following research titles: A CMOS ultra-wide band low noise amplifier based on cascode topology, Power consumption reduction in the design of CMOS ECG bio-amplifier for health monitoring purposes, Design of low power high speed CMOS digital multiplier using vedic mathematics method, and A 16-bit pipeline analog-to-digital IC converter for biomedical application. A member for the following research titles: Performance enhancement for TFET under power constraints using DVFS, Development of a cold atmospheric plasma system for oral cancer microtissue treatment, Load coil and isolation resistance effect study by laboratory testing and CST cable simulation, New approach of hardware modeling by using FPGA for cardiac reentrant arrhythmia real-time analysis tool, Investigation of low power design techniques for deep submicron CMOS circuits, and Sensor for animal pathogen detection system using MEMS technology on low power IC design.

*****All the columns need to be filled in.