


Brief CV

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|--------------------------------------|--|----------------------------|----------|---|
| Name | Ajay Kumar Singh | 中文名 | |  |
| Gender | M | Title (Pro./Dr.) | DR | |
| Position (President...) | ASSOCIATE PROFESSOR | Country | MALAYSIA | |
| WeChat/WhatsApp /FB | | | | |
| University/ Department | Faculty of Engineering and Technology-Multimedia University | | | |
| Personal Website | | | | |
| Research Area | VLSI IC DESIGN, MODELING OF MOS DEVICES, CNTFET DEVICES, RENEWABLE ENERGY | | | |

Brief introduction of your research experience:

I have more than 24 years of teaching and research experience in the field of Electronics Engineering. My research areas of interest are, IC design, Digital VLSI design, Modelling of MOS devices, Carbon Nano tube FETs and Renewable energy source. I have published around 80 research papers in International journals and conferences and contributed two chapters in the research book. I have successfully completed two major research projects on the design of 32 Kb SRAM memory cache and Modeling of Carbon Nanotube Field Effect Transistor. I am in Editorial team of many international journals and served in many technical programme committee as a member. I am Senior Member of IEEE and Fellow of the Institution of Electronics and Telecommunication Engineers, India. I have successfully supervised 4 PhD thesis and 3 Master thesis.

*****All the columns need to be filled in.